

# CALL FOR APPLICATIONS

## Faculty Position at the Department of Electronic Engineering, Universidad Técnica Federico Santa María (UTFSM), Valparaíso, Chile

This call is aimed at filling a full-time faculty position for the Department of Electronic Engineering at UTFSM in the field of Telecommunications and Radio Frequency (RF).

The selected candidate should conduct research in areas related to Telecommunications and RF, either in fundamental research in the discipline (e.g. wave propagation, antennas, and microwave engineering) or applied engineering such as personal communication systems (e.g. 5G, millimeter waves), satellite communications, radio astronomy, and RF applications in industry.

**Benefits:** UTFSM offers an attractive starting salary, around 40,000 USD per year, depending on initial academic evaluation<sup>1</sup>. Additional benefits include free lunch, supplemental health insurance and incentives for publications in Web of Science (WoS) indexed journals. Additional economic incentives are possible, if the candidate has been granted a research project founded by the national scientific and research agency ANID<sup>4</sup>.

**Job Responsibilities:** The successful candidate is expected to be a competent and highly motivated academic, who will contribute to teaching in undergraduate and graduate (Master and Doctorate) programs and to exhibit research and innovation capabilities. The willingness of the candidate to perform theoretical and experimental teaching and experience in projects of multidisciplinary basic and applied research is particularly encouraged.

A performance agreement will be established for an interim period of one, two, or three years depending on the candidate's academic experience, establishing teaching and research goals to be achieved in that time frame. If necessary, the interim agreement will also provide a Spanish language training program, to achieve language proficiency for teaching purposes. Fulfilling the agreement goals will enable the candidate to participate on tenure track.

### Application requirements

Candidates may be Chilean or foreign nationals, in possession of a doctoral degree or in the final stages of receiving it. They must also meet the following additional requirements:

1. The trajectory and expertise of the candidate must be consistent with the teaching and research duties in the fields advertised.
2. To have teaching abilities and to be willing to teach at undergraduate level (theoretical and experimental subjects).
3. To be willing to participate in training programs aimed at the use of modern educational methodologies and also in the planning of the engineering education curriculum.
4. Exhibit demonstrable results in the creation of knowledge, through publications in WoS indexed journals, international conferences, or patents.
5. Writing and speaking proficiency in English and Spanish. Applicants not fluent in Spanish should show willingness to acquire within one-year sufficient proficiency to be able to teach and interact with students.

### Documentation that must be included in the application

1. Curriculum Vitae.
2. Formulation of a personal proposal for academic development in research and teaching.
3. Undergraduate and graduate grade transcripts.
4. Certificates of diplomas and degrees.
5. Contact information of two senior academics who are willing to provide academic and personal references. One of the referees should be the Ph.D. thesis advisor. These references will be requested directly by the Head of the Department of Electronic Engineering, UTFSM.

## Selection Procedure

The Department of Electronic Engineering will select and interview candidates to form a shortlist. All those in the shortlist will be formally interviewed by a Selection Committee and should present a public lecture on a topic chosen in agreement between the applicant and the Department. Videoconference will be used if a visit to UTFSM cannot be arranged. The Selection Committee will then rank these candidates, for the University authority to make the final decision.

## Submission of Applications and Important Dates

Applications should be sent in electronic format to: [postulaciones.ddrrhad@usm.cl](mailto:postulaciones.ddrrhad@usm.cl), sending a copy to: [concursos.elo@usm.cl](mailto:concursos.elo@usm.cl). The subject of the message should include the position code: 22-01-74.

Application Deadline: July 15th, 2022

Preliminary Selection (shortlist) will be informed by September 1st, 2022

The selected candidate could fill the position starting December 1st, 2022, or earlier depending on availability.

## Additional information

**Universidad Técnica Federico Santa María (UTFSM or USM, in short)** was the first Latin American university to offer a doctoral program in Engineering (since 1962). USM is one of the oldest and traditional public universities in Chile, with more than 90 years of history. Since its foundation, the University has been focused on the development of Science, Engineering and Technology. Our University has a strong emphasis on research and on scientific and technological education. USM is consistently ranked among the top three engineering schools of the country; it is ranked first place nationally in Engineering and Technology (US News Education – Best Global Universities 2020: Engineering, Times Higher Education (THE) WUR 2019: Engineering and Technology), 9th place in Latin America (US News Education – Best Global Universities Latin America 2020). In the THE Impact Ranking 2020, it is ranked in 34th worldwide. In QS World University Ranking 2020-2022 by Subject: Engineering and Technology, it is ranked in 3rd place nationally.

In **Electrical and Electronic Engineering, UTFSM** was ranked number one in Latin America at the Shanghai Academic Ranking of World Universities for the years 2017 to 2021, and in the range 101 to 150 worldwide<sup>1</sup>. In the same ranking in 2017, UTFSM is also considered number one in Latin America in the subjects of Instruments Science and Technology (47 in the World), and Automation and Control (76 to 100 Worldwide)<sup>2</sup>. In the 2020 QS World University indicator in Electrical and Electronic Engineering, UTFSM ranked in the position 201-250 worldwide and 3rd place in Chile. Also, in the 2021-2022 QS World University indicator in Electrical and Electronic Engineering, UTFSM ranked 3rd place in Chile<sup>3</sup>.

**The Electronic Engineering Department** of UTFSM is mainly located at the **Main Campus** of the **Universidad Técnica Federico Santa María, in Valparaíso, Chile. Valparaíso** is a major city, seaport and university center as well as an important touristic attraction, being declared a UNESCO World Heritage Site and Chile's cultural capital<sup>4</sup>. According to the United Nations Development Programme, **Chile** is one of South America's most stable and prosperous nations, leading Latin American nations in human development<sup>5</sup>, competitiveness, income per capita, globalization and economic freedom. Since July 2013, Chile is considered by the World Bank as a "high-income economy", and hence as a developed country<sup>7</sup>.

Activities of the Department of Electronic Engineering are described at the websites: [www.electronica.usm.cl](http://www.electronica.usm.cl) and [www.telematica.usm.cl](http://www.telematica.usm.cl). General information on the University and on the city of Valparaíso and surroundings can be found in <http://oai.usm.cl/en/usm>. Further questions are welcome and can be formulated to: [concursos.elo@usm.cl](mailto:concursos.elo@usm.cl).